TUBF0204 Product Brief

1. Description

The TBUF0204 is a high-performance CMOS Clock Buffer Family. It has low skew and supports a synchronous glitch-free output enable function to eliminate any potential intermediate incorrect output clock cycles when enabling or disabling outputs. It can operate from a 1.5V to 3.3V supply.

2. Applications

- IT infrastructure
- 5g communication
- Automotive electronic

3. Key Features

- 1 to 4 fanout buffer
- Low skew outputs (250ps)
- Low power CMOS technology
- Supply voltage support 1.5V to 3.3V
- Output Enable pin tri-states outputs
- 3.6V tolerant input clock
- Totally Lead-Free & Fully RoHS Compliant
- Halogen and Antimony Free
- Packaged: 8-pin SOP

4. Functional Diagram

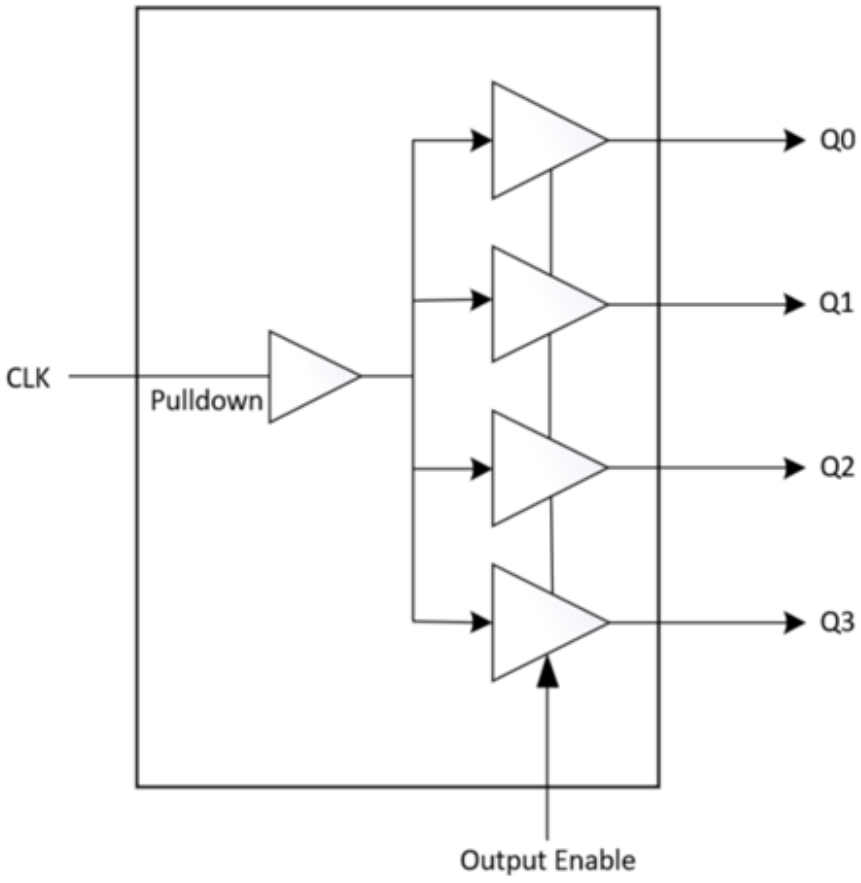

Figure 1 Functional Diagram

5. Pin Maps

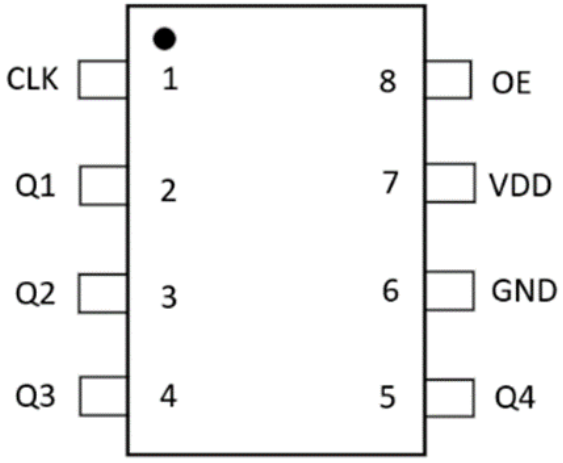

Figure 2 Pin Assignment Diagram- SOP8

6. Pin Descriptions

Table 1 Pin Descriptions

Pin number	Pin name	Type	Description
1	CLK	input	Clock Input. 3.3 V tolerant input. Internal 51kΩ pull down resistor.
2	Q1	output	Clock output pin 1.
3	Q2	output	Clock output pin 2.
4	Q3	output	Clock output pin 3.
5	Q4	output	Clock output pin 4.
6	GND	power	Connect to ground.
7	VDD	power	Connect to 1.5V,1.8V,2.5V or 3.3V.
8	OE	input	Output Enable. Tri-states outputs when low. Connect to V_{DD} for normal operation.